

CLAIM AMENDMENTS

Please cancel claims 1, 13-15, 20-21, 23, 27-28, and 30 without prejudice or disclaimer.

Please amend claims 16, 22, 24, and 29 as follows.

1-15. (Canceled)

16. (Currently Amended) A method for implementing a soft reset in an active pixel sensor, the active pixel sensor including a sensor which produces a sensor potential and a reset transistor coupled to the sensor, the method comprising:

(a) determining a selected critical level according to the critical potential at which the reset transistor will be on when the soft reset function begins;

(b) pulling down the sensor potential below the selected critical level before the soft reset function is performed; and

(c) implementing the soft reset function to reset the sensor potential to a selected reset level, wherein the sensor is coupled through a plurality of transistors to a bit line, and the bit line is used to pull down the sensor potential;

wherein a loading transistor is coupled to the bit line, and the voltage potential on the bit line is pulled down by increasing bias on the loading transistor; and

wherein a pull-up transistor is coupled to the gate of the loading transistor, and the pull-up transistor is used to increase the bias on the loading transistor.

17. (Original) The method of claim 16, wherein during step (b) the sensor potential is stabilized below the critical level before the soft reset begins at step (c).

18-21. (Canceled)

22. (Currently Amended) The method of claim [[21]] 16, wherein the gate of the loading transistor is coupled to a biasing circuit, and the biasing circuit is used to increase the bias on the loading transistor.

23. (Canceled).

24. (Currently Amended) An active pixel sensor circuit in which a soft reset function is performed, the active pixel sensor circuit comprising:

- a sensor which outputs a sensor potential;
- a reset transistor coupled to the sensor; [[and]]
- a bit line coupled through a plurality of transistors to the sensor, wherein the sensor potential is pulled below a selected critical level prior to the time when a soft reset function is performed to reset the sensor potential, wherein the bit line is used to pull the sensor potential below the selected critical level, and wherein the bit line is coupled to a loading transistor, the voltage potential on the bit line being pulled down by increasing bias on the loading transistor;
and
a pull-up transistor coupled to the gate of the loading transistor, the pull-up transistor increasing the bias on the loading transistor so as to pull down the voltage potential on the bit line.

25-28. (Canceled).

29. (Currently Amended) The active pixel sensor circuit of claim [[28]] 24, further comprising a biasing circuit coupled to the loading transistor, the biasing circuit being used to increase the bias on the loading transistor so as to pull down the voltage level on the bit line.

30. (Canceled).

31. (Original) The active pixel sensor circuit of claim 24, wherein the selected critical level is determined according to the potential at which the reset transistor will be on when the soft reset function begins.